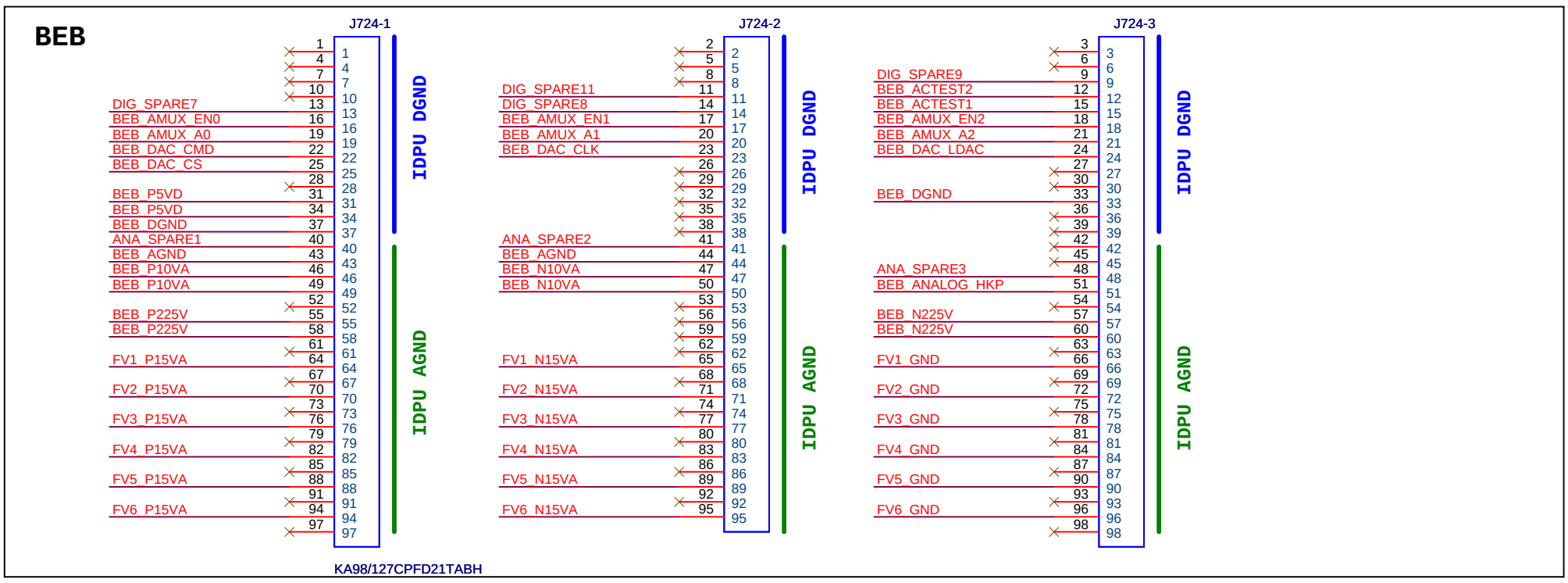
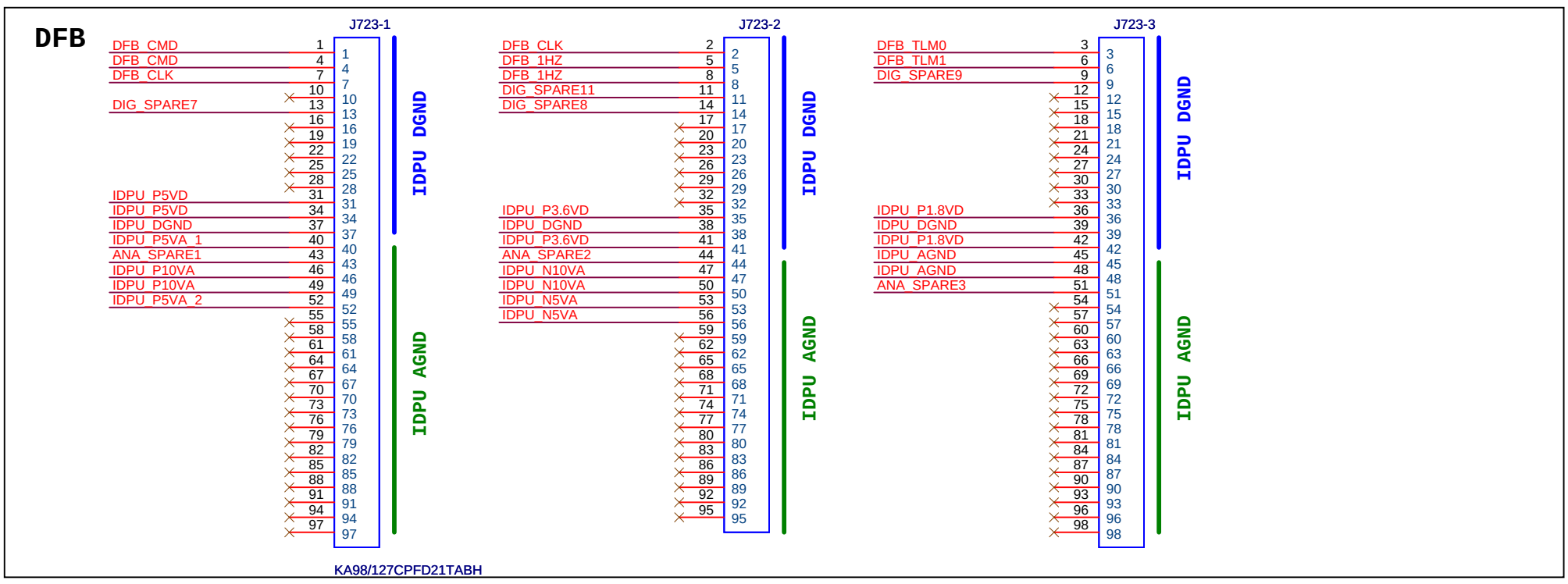
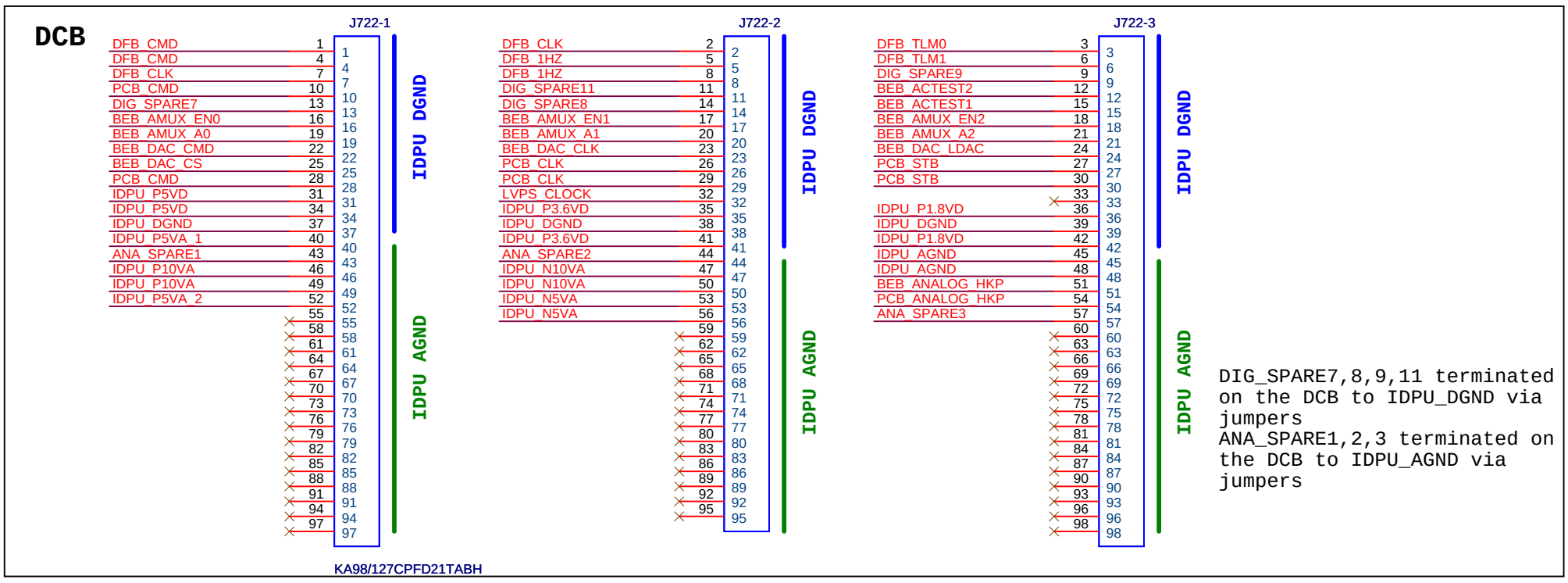
## BEB




## DFB

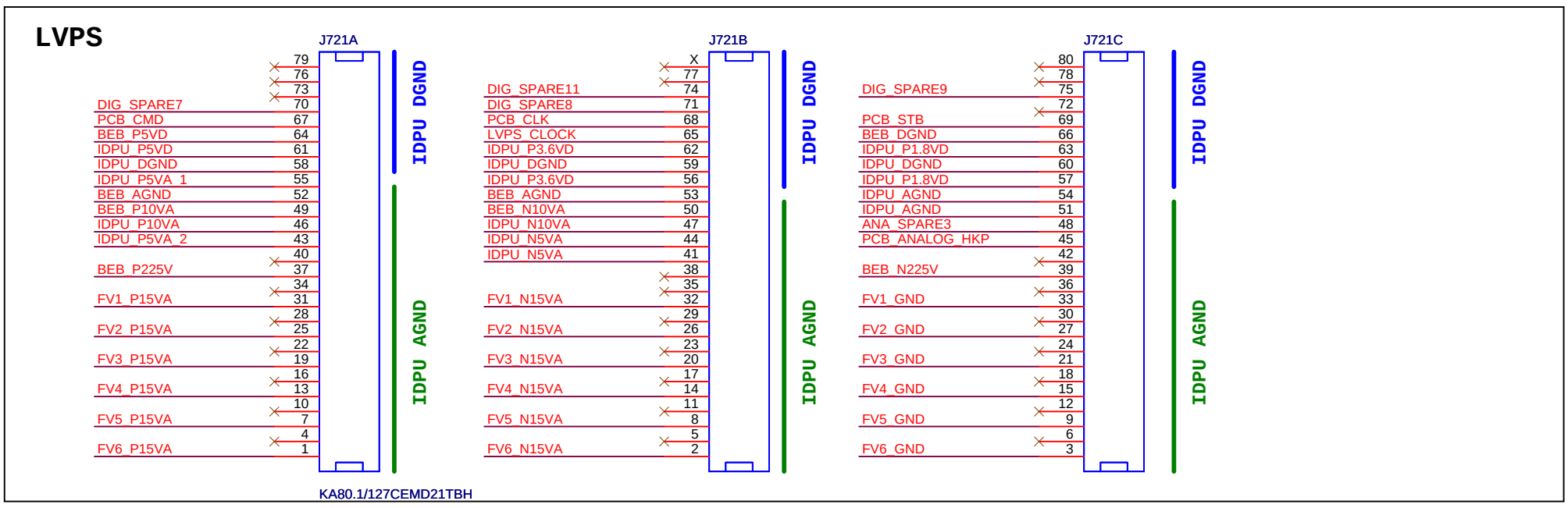



## DCB




DIG_SPARE7,8,9,11 terminated on the DCB to IDPU_DGND via jumpers
ANA_SPARE1,2,3 terminated on the DCB to IDPU_AGND via jumpers

## LVPS




**Board Sizing is per mechanical backplane specification.**

**Board contains traces on inner layers sandwiched by ground planes split between IDPU Analog and Digital Ground as shown above.**

| | | |
|---|---|---|
| Title | RBSP EFW BACKPLANE SCHEMATIC | |
| Size | Document Number RBSP_EFW_BPL_003L_Schematic | Rev L |
| Date: Friday, August 14, 2009 | Sheet 1 of 1 | |